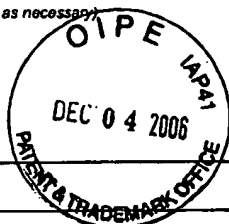


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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

|                      |                  |
|----------------------|------------------|
| Application Number   | 09/644,463       |
| Filing Date          | August 23, 2000  |
| First Named Inventor | Haycock, Matthew |
| Group Art Unit       | 2111             |
| Examiner Name        | Phan, Raymond    |

Sheet 1 of 1

Attorney Docket No: 884.303US1

**US PATENT DOCUMENTS**

| Examiner Initial * | USP Document Number | Publication Date | Name of Patentee or Applicant of cited Document | Filing Date If Appropriate |
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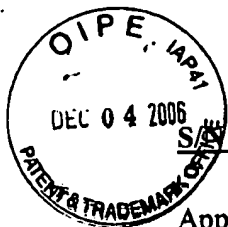
EXAMINER

RAYMOND PHAN

DATE CONSIDERED 12-19-06

Substitute Disclosure Statement Form (PTO-1449)

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached



S/N 09/644,463

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Matthew B. Haycock et al. Examiner: Raymond Phan  
Serial No.: 09/644,463 Group Art Unit: 2111  
Filed: August 23, 2000 Docket: 884.303US1  
Title: SIMULTANEOUS BIDIRECTIONAL PORT WITH SYNCHRONIZATION  
CIRCUIT TO SYNCHRONIZE THE PORT WITH ANOTHER PORT

COMMUNICATION CONCERNING RELATED APPLICATION(S)

MS RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

| <u>Serial/Patent No.</u> | <u>Filing Date/Issue Date</u> | <u>Attorney Docket</u> | <u>Title</u>   |
|--------------------------|-------------------------------|------------------------|--|
| 09659499<br>6424175      | September 11, 2000            | 884.307US1             | BIASED CONTROL LOOP CIRCUIT<br>FOR SETTING IMPEDANCE OF<br>OUTPUT DRIVER                                   |
| 09694901<br>6445170      | October 24, 2000              | 884.333US1             | CURRENT SOURCE WITH INTERNAL<br>VARIABLE RESISTANCE AND<br>CONTROL LOOP FOR REDUCED<br>PROCESS SENSITIVITY |
| 09824370<br>6448811      | April 2, 2001                 | 884.403US1             | INTEGRATED CIRCUIT CURRENT<br>REFERENCE  |
| 09835600<br>6507225      | April 16, 2001                | 884.450US1             | CURRENT MODE DRIVER WITH<br>VARIABLE EQUALIZATION  |
| 09951909<br>6529037      | September 13, 2001            | 884.508US1             | VOLTAGE MODE BIDIRECTIONAL<br>PORT WITH DATA CHANNEL USED<br>FOR SYNCHRONIZATION                           |
| 09894865<br>6791356      | June 28, 2001                 | 884.509US1             | BIDIRECTIONAL PORT WITH CLOCK<br>CHANNEL USED FOR<br>SYNCHRONIZATION                                       |
| 09972327<br>6597198      | October 5, 2001               | 884.527US1             | CURRENT MODE BIDIRECTIONAL<br>PORT WITH DATA CHANNEL USED<br>FOR SYNCHRONIZATION                           |
| 10690235<br>6803790      | October 21, 2003              | 884.509US2             | BIDIRECTIONAL PORT WITH CLOCK<br>CHANNEL USED FOR<br>SYNCHRONIZATION                                       |

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12-19-06